

10/00214
 11/01/01
 S3D

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10002714	11/01/2001	427		1762	JALDO H. PATEL

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*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/454,023 12/03/1999
WHICH IS A CON OF 09/177,856 10/23/1998 ABN

* FOREIGN APPLICATIONS VERIFIED:
EUROPEAN PATENT OFFICE (EPO) 00123976.3 11/03/2000

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no
Attorney Docket No MCGEPO176USA		
Verified and Acknowledged Examiner's initials		
TITLE : Tin whisker-free printed circuit board		
U.S. DEPT. OF COMM./PAT & TM-PTO-425L(Rev. 12-01)		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig. 10/16
<input type="checkbox"/> TERMINAL		Primary Examiner		
DISCLAIMER		Application Examiner		
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